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The 25xN RapidIO Specification 4.1 offers 100 gigabits per second (Gbps) per x4 port to provide a proven, open standard interconnect fabric for systems requiring heterogeneous ... and custom and ...

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Intel to Invest \$3.5 Billion to Expand New Mexico Manufacturing Operations

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Intel said its \$3.5 billion New Mexico manufacturing expansion will boost its ability to make next-generation chips with its Foveros 3D packaging technology.

Intel ' s \$3.5B New Mexico Campus Upgrade To Boost Next-Gen Chips

for a system. Computation, memory, and interconnect all need to be balanced. Otherwise, resources are wasted and performance is lost. The problem is that the interconnect is rarely seen as a ...

Interconnects In A Domain-Specific World

Compute Express Link is a cache-coherent interconnect for processors, memory expansion, and accelerators that maintains a unified coherent memory space between the CPU and any memory on

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As advanced algorithms continue to emerge for smart product designs, developers often find themselves struggling to implement embedded ...

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Data center operators similarly shunned Arm processors as too underpowered until AWS built the Graviton1 SoC around Arm's first 64-bit v8 architecture. However, with sub-10nm process nodes enabling 64 ...

Armed and serious - telcos take note that Arm is serious about infrastructure and the cloud

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Most of the time this conversation centers around problems with quantum effects in scaled-down devices; however, another (and arguably more significant) problem is the interconnect, which is ...

Silicon Photonics May Remedy the Interconnect Bottlenecks of Moore ' s Law

Embedded die packaging is seeing renewed demand amid the push towards chips and systems that require smaller form factors ... technology in general are increased miniaturization, reliable ...

Embedded Die Packaging Emerges

This is the first in its family of server CPU cores based on the Arm v9 architecture, bringing with it major improvements in performance, power efficiency, and security.

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Arm Clashes With Intel and AMD With Neoverse N2 Server CPUs

Market research reports show that 10% or more of all FPGAs are used by military and aerospace systems. Clearly FPGA provides major advantages in the flexibility to adapt to changing standards and ...

The Advantages of Embedded FPGA for Aerospace and Defense

Renesas expands its timing solutions portfolio with a sub-100fs point-of-use clock solution for data center, server and network infrastructure markets ...

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For years, sensors and data capture capabilities have been designed,

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built and embedded into production lines ... Now there is an opportunity to interconnect all of these data collection points and ...

Is the Internet of Things the Next Industrial Revolution?

They are currently the only FPGAs in their class to support embedded MIPI D-PHY interfaces with speeds up to 10 Gbps. “ As automotive systems grow in capability and complexity, the number of integrated ...

Lattice unveils Embedded Vision FPGA for automotive applications

Smiths Interconnect, the RF systems specialist for Aerospace and Defense, has signed a contract with Mitsubishi Electric Corporation to participate in the development of a G-band satellite for the ...

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